

A1
Control

comparing the two sets of results and providing an output signal indicating if the model is inaccurate if the results contradict.

A2

6. (Amended) A method according to [any preceding] claim 1, further comprising the steps of, during a process of verifying the accuracy of the model, said model being a digital model, comparing the results of the model with the results from the simulation of an analog model of the circuit, identifying whether the digital model is an accurate model and only comparing the digital model results with the [analogue] analog model results if the digital model is determined in said comparing step to be accurate.

8. (Amended) A method according to [any preceding] claim 1, further comprising the steps of during simulation for the plurality of signals determining the value of each output from said model; and
during resimulation determining for the expanded set the value of each output from the model.

9. (Amended) A method as claimed in [any preceding] claim 1, wherein said model is a digital model.

A3

12. (Amended) A computer program comprising program code [means for performing] that, when executed on a computer, performs any of the steps of any of claims 1 to 9 [when the program is run on a computer].